

Extending NUXMV with Timed Transition Systems and Timed Temporal Properties

A. Cimatti, A. Griggio, E. Magnago, M. Roveri, and S. Tonetta

Fondazione Bruno Kessler

Abstract. NUXMV is a well-known symbolic model checker, which implements various state-of-the-art algorithms for the analysis of finite- and infinite-state transition systems and temporal logics. In this paper, we present a new version that supports timed systems and logics over continuous super-dense semantics. The system specification was extended with clocks to constrain the timed evolution. The support for temporal properties has been expanded to include $MTL_{0,\infty}$ formulas with parametric intervals. The analysis is performed via a reduction to verification problems in the discrete-time case. The internal representation of traces has been extended to go beyond the lasso-shaped form, to take into account the possible divergence of clocks. We evaluated the new features by comparing NUXMV with other verification tools for timed automata and $MTL_{0,\infty}$, considering different benchmarks from the literature. The results show that NUXMV is competitive with and in many cases performs better than state-of-the-art tools, especially on validity problems for $MTL_{0,\infty}$.

1 Introduction

NUXMV [1] is a symbolic model checker for the analysis of synchronous finite- and infinite-state transition systems. For the finite-state case, NUXMV features strong verification engines based on state-of-the-art SAT-based algorithms. For the infinite-state case, NUXMV features SMT-based verification techniques, implemented through a tight integration with the MATHSAT5 solver [2]. NUXMV has taken part to recent editions of the hardware model checking competition, where it has shown to be very competitive with the state-of-the-art. NUXMV also compares well with other model checkers for infinite-state systems. Moreover, it has been successfully used in several application domains both in research and industrial settings. It is currently the core verification engine for many other tools (also industrial ones) for requirements analysis, contract based design, model checking of hybrid systems, safety assessment, and software model checking.

NUXMV is under continuous development, and various new functionalities have been added over the years since [1]. The most noticeable are: i) support for uninterpreted functions and unbounded arrays; ii) support for non-linear dynamics and transcendental functions [3]; iii) integrated several advanced model checking algorithms (e.g. [3,4,5]).

In this paper, we put emphasis on the novel extensions to NUXMV to support timed synchronous transition systems, which extend symbolically-represented infinite-state transition systems with clocks. The main novelties of this new version are the following. The NUXMV input language was extended to enable the description of symbolic

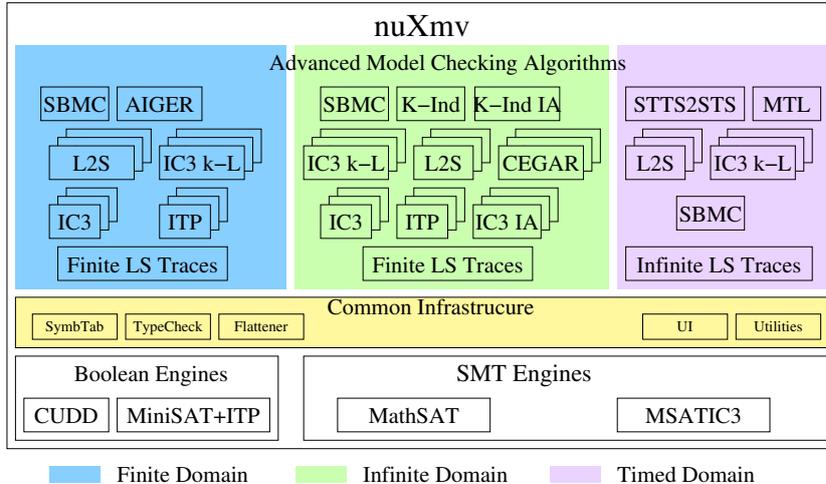


Fig. 1. The high level architecture of NUXMV.

synchronous timed transition systems with super-dense time semantics (where signals can have a sequence of values at any real time t). The support for temporal properties has been expanded to include $MTL_{0,\infty}$ formulas with parametric intervals [6,7]. Therefore, NUXMV now supports model checking of invariant, LTL and $MTL_{0,\infty}$ properties over (symbolic) timed transition systems, as well as validity/satisfiability checking of LTL and $MTL_{0,\infty}$ formulas. This is done via a correct and complete reduction to verification problems in the discrete-time case (thus allowing for the use of mature and efficient verification engines). In order to represent and find infinite traces where clocks may diverge, we extended the representation for lasso-shape traces (over discrete semantics) and we modified the bounded model checking algorithm to properly encode timed traces. We remark that, NUXMV is more expressive than timed automata, since the native management of time is added on top of an infinite state transition system. This makes it straightforward to encode stopwatches and comparison between clocks. We carried out an experimental evaluation comparing NUXMV with other state-of-the-art verification tools for timed automata, considering different benchmarks taken from competitor tools distributions.

2 Software Architecture

The high level architecture of NUXMV is depicted in Fig. 1. For symbolic transition systems NUXMV behaves like the previous version of the system [1], thus allowing for full backward compatibility (apart from some new reserved keywords). It provides the user with all the basic model checking algorithms for finite domains both using BDDs (using CUDD [8]) and SAT (e.g. MINISAT [9]). It supports various SMT-based model checking algorithms (implemented through a tight integration with the MATHSAT5 solver [2]) for the analysis of finite and infinite state systems (e.g. IC3 [10,11,12], k-liveness [13], liveness to safety [14]). We refer the reader to [1] for a thorough discussion of these consolidated functionalities for the discrete-time setting.

```

1 @TIME_DOMAIN continuous — annotation to specify the time semantics, in this case dense time
2
3 MODULE main
4 FROZENVAR p: real; INIT p > 0 — parameter
5 VAR i: real; — input of the sensor
6 VAR s: Sensor(i);
7 VAR m: Monitor(s.o,p);
8
9 LTLSPEC G ( s.fault -> F [0,p] m.alarm ) — any fault is detected in p timed units
10
11 MODULE Sensor(i)
12 VAR o: real;
13 VAR fault: boolean;
14 TRANS !fault -> next(o) = i — if not faulty, the sensor provides in output directly the input
15 TRANS fault -> next(o) = o — if faulty, the sensor output is stuck at the last value
16 TRANS fault -> next(fault) — the fault is permanent
17
18 MODULE Monitor (i,p)
19 VAR previous_value: real;
20 VAR c: clock;
21 VAR alarm: boolean;
22 INIT c=0 & previous_value = i & !alarm
23 INVARIANT TRUE -> c <= p
24 TRANS time <= p | time >= p
25 TRANS (c = p & next(c) = 0 & next(previous_value) = i) | — the monitor reads the sensor every p time units
26 (c <= p & next(c) = c & next(previous_value) = previous_value)
27 TRANS next(alarm) <-> (alarm | i=previous_value) — alarm raised when the same value read twice consecutively

```

Fig. 2. A simple TIMED-NUXMV program.

To support the specification and model checking of invariant, LTL and $MTL_{0,\infty}$ properties for timed transitions systems, and for the validity checking of properties over dense time semantics, NUXMV has been extended w.r.t. [1] as discussed here after.

- We extended the parser to allow the user to choose the time semantics to use for the read model. Depending on the time model some parse constructs and checks are enabled and/or disabled. For instance, variables of type clock and $MTL_{0,\infty}$ properties are only allowed if the dense time semantics has been specified. By default the system uses the discrete time semantics of the original NUXMV. Notice also that, depending on the specified semantics, the commands available to the user change to allow only the analyses supported for the chosen semantics.
- We extended the parser to support the specification of symbolic timed automata (definition of clock variables, specification of urgent transitions and state invariants, etc.). Moreover, we extended the parser to allow for the specification of $MTL_{0,\infty}$ properties, and we extended the LTL bounded operators not only to contain constants, but also complex expressions over clock variables. See Fig. 2 for a simple example showing some of the new language constructs.
- We extended the symbol table to support the specification of clock variables, and we extended the type checker to properly handle the new defined variables, expression types and language constructs.
- We added new modules for the encoding of the symbolic timed automata into equivalent transition systems to verify with the existing algorithms of NUXMV.
- We extended the traces for NUXMV to support timed traces (lasso-shaped traces where some clock variables may diverge).
- We modified the encoding for the loops in the bounded model checking algorithms to take into account that traces may contain diverging variables to allow for the verification and validation of LTL and $MTL_{0,\infty}$ properties.

We remark that, the above extensions have been complemented with several additional new functionalities since [1]. The most noticeable are: i) the support for uninterpreted

functions and unbounded arrays; ii) the support for non-linear dynamics and transcendental functions [3]; iii) the integration of several advanced symbolic model checking algorithms for infinite state systems [3,4,5].

For portability, NUXMV has been developed mainly in standard C with some new parts in standard C++. It compiles and executes on Linux, MS Windows, and MacOS.

3 Language Extensions

Timed Transition Systems Discrete-time transition systems are described in NUXMV by a set V of variables, an initial condition $I(V)$, a transition condition $T(V, V')$ and an invariant condition $Z(V)$. Variables are introduced with the keyword `VAR` and can have type Boolean, scalar, integer, real or array. The initial and the invariant conditions are introduced with the keyword `INIT` and `INVAR` and are expressions over the variables in V . The transition condition is introduced with `TRANS` and is an expression over variables in V and V' , where for each variable in V , V' contains the “next” version denoted in the language by `next(v)`. Expressions may use standard symbols in the theory associated to the variable types and user-defined rigid functions that are declared with the keyword `FUN`.

The input language of NUXMV has been extended to allow the specification of timed transition systems (TTS), which are enabled by the annotation `@TIME_DOMAIN continuous` at the beginning of a model description.

Besides the standard types, in the timed case, state variables can be declared of type `clock`. All variables of type different from `clock` are discrete variables.

The language provides a built-in `clock` variable, accessible through the reserved keyword `time`. It represents the amount of time elapsed from the initial state until now. `time` is initialized to 0 and its value does not change in discrete transitions. While all other `clock` variables can be used in any expression in the model definition, `time` can be used only in comparison with constants.

Initial, transition, and invariant conditions are specified in NUXMV with the keywords `INIT`, `TRANS`, and `INVAR`, as in the discrete case. In particular, `TRANS` allows to specify “arbitrary” clock resets. Like all other NUXMV state variables, if a clock is not constrained during a discrete transition, its next value is chosen non-deterministically.

Clock variables can be used in `INVAR` only in the form $\varphi \rightarrow \phi$, where φ is a formula built using only the discrete variables and ϕ is convex over the clock variables. This closely maps the concept of location invariant described for timed automata: all locations satisfying φ have invariant ϕ .

An additional constraint, not allowed in the discrete-time case, is introduced with the keyword `URGENT` followed by a predicate over the discrete variables, which allows to specify a set of locations in which time cannot elapse.

Comparison with Timed Automata Timed automata can be represented by TTSs by simply introducing a variable representing the locations of the automaton. Note that, in TTS, it is possible to express any kind of constraint over clock variables in discrete transitions, while in timed automata it is only possible to reset them to 0 in transitions or compare them to constants in guards. Moreover, the discrete variables of a timed automaton always have finite domain, while in TTSs, also the discrete variables might have an infinite domain. This additional expressiveness allows to describe more

complex behaviors (e.g. it is straightforward to encode stopwatches and comparison between clocks) losing the decidability of the model checking problem.

Specifications NUXMV’s support for LTL has been extended to allow for the use of $MTL_{0,\infty}$ operators [15] and other operators such as event-freezing functions [16] and dense version of LTL X and Y operators. $MTL_{0,\infty}$ bounded operators extend the LTL ones of NUXMV to allow for bounds either of the form $[c,\infty)$, where c is a constant greater or equal to 0, e.g. $F[0,+\infty)\ \varphi$, or generic expressions over parametric/frozen variables: e.g. $F[0,3+v]\ \varphi$ where v is a frozen variable.

In timed setting, next and previous operators come in two possible versions. The standard LTL operators X and Y require to have, respectively after and before, a discrete transition. Dually, X^- and Y^- have been introduced to allow to predicate about the evolution over time of the system. They are always `FALSE` in discrete steps and hold in time elapses if the argument holds in the open interval immediately after/before the current step. The disjunction of these operators $X(\varphi) \vee X^-(\varphi)$ allows to check if the argument φ holds after the current state without distinction between time or discrete evolution.

The event-freezing operators **at next** and **at last**, written $@\tilde{F}$ and $@\tilde{O}$, are binary operators allowed in LTL specifications. The left-hand side is a term, while the right-hand side is a temporal formula. They return the value of the term respectively at the next and at the last point in time in which the formula is true. If the formula will [has] never happen [happened] the operator evaluates to a default value.

`time.until` and `time.since` are two additional unary operators that can be used in LTL specifications of timed models. Their argument must be a Boolean predicate over current and next variables. `time.until`(φ) evaluates to the amount of time elapse required to reach the next state in which φ holds, while `time.since`(φ) evaluates to the amount of time elapsed from the last state in which φ held. As for the $@\tilde{F}$ and $@\tilde{O}$ operators if no such state exists they are assigned to a default value.

4 Extending Traces

Timed Traces The semantics of NUXMV has been extended to take into account the timing aspects in case of super-dense time. While in the discrete time case, the execution trace is given by a sequence of states connected by discrete transitions (i.e., satisfying the transition condition), in the super-dense time case the execution trace is such that every pair of consecutive states is a discrete or a timed transition. As in the discrete case, discrete transitions are pair of states satisfying the transition condition. As in timed automata, in a timed transition time elapses for a certain amount (referred to as `delta_time`), clocks increase of the same amount, while discrete variables do not change.

Lasso-shaped traces with diverging variables Traditionally, the only infinite paths supported by NUXMV have been those in lasso shape, i.e. those traces which can be represented by a finite prefix s_0, s_1, \dots, s_l (called the stem) followed by a finite suffix $s_{l+1}, \dots, s_k \equiv s_l$ (called the loop), which can be repeated infinitely many times. While this representation is sufficient for finite-state systems (because in a finite-state setting if a system does not satisfy an LTL property, then a lasso-shaped counter-example trace is guaranteed to exist), this is an important limitation in an infinite-state context, in

which lasso-shaped counter-examples are not guaranteed to exist. (As a simple example, consider a system $M := \langle \{x\}, (x = 0), (x' = x + 1) \rangle$ in which $x \in \mathbb{Z}$. Then $M \not\models \mathbf{GF}(x = 0)$, but clearly M has no lasso-shaped trace.) In fact, this is especially relevant for timed transition systems, which, by the presence of the always-diverging variable *time*, admit *no* lasso-shaped trace.

In order to overcome this limitation, we introduce a new kind of infinite traces, which we call *lasso-shape traces with diverging variables* (to allow also for representing traces with variables whose value might be diverging). We modified the bounded model checking algorithms to leverage on this new representation to then extend the capabilities to find witnesses for a given property. This representation significantly extends the capabilities of NUXMV to find witnesses for violated LTL and MTL properties on timed transition systems (see experimental evaluation).

Definition 1 Let $\pi := s_0, s_1, \dots, s_l, \dots$ be an infinite trace of a system M over variables V . We say that π is a lasso-shaped trace with diverging variables iff there exist indexes $0 \leq l \leq k$, a partitioning of V into sets X and Y ($V = X \uplus Y$) and an expression $f_y(V)$ over V for every variable $y \in Y$ such that, for every $i > k$,

$$s_i(v) := \begin{cases} s_{l + ((i-l) \bmod (k-l))}(v) & \text{if } v \in X \text{ (like in lasso-shaped traces)} \\ f_v(s_{i-1}) & \text{if } v \in Y \text{ (as function of previous state)} \end{cases}$$

Intuitively, the idea of lasso-shaped traces with diverging variables is to provide a finite representation for infinite traces that is more general than simple lasso-shaped ones, and which allows to capture more interesting behaviors of timed transition systems.

Example 1. Consider the system $M := \langle \{y, b\}, \neg b \wedge y = 0, (b' = \neg b) \wedge (b \rightarrow y' = y + 1) \wedge (\neg b \rightarrow y' = y) \rangle$. Then one lasso-shaped trace for M is given by: $\pi := s_0, s_1, s_2$, where $s_0 := \{b \mapsto \perp, y \mapsto 0\}$, $s_1 := \{b \mapsto \top, y \mapsto 0\}$, and $s_2 := \{b \mapsto \perp, y \mapsto 1\}$; the trace is lasso-shaped with diverging variables considering $Y := \{y\}$; the loop-back at index 0, and $f_y(b, y) := b ? y + 1 : y$.

Extended BMC for Traces with Divergent Clocks The definition above requires the existence of the functions f_y for computing the updates of diverging variables. In case y is a clock variable, we can define a region $\llbracket \phi_y \rrbracket$ in which y can diverge (i.e., $f_y = y + \delta$, where δ is the delta time variable).

In order to capture lasso-shaped traces with diverging variables, we can modify the BMC encoding as follows. Let $\bigvee_{l=0}^k (\bigwedge_{v \in X \uplus Y} (v^l = v^k) \wedge \llbracket \varphi \rrbracket_k^0)$ be the formula representing the BMC encoding of [17] at depth k with all possible loop-backs $0 \leq l \leq k$ for a given formula φ . The encoding is extended as follows:

$$\bigvee_{l=0}^k \left(\left(\bigwedge_{x \in X} (x^l = x^k) \wedge \bigwedge_{y \in Y} (y^l = y^k \vee \bigwedge_{i=l}^k \llbracket \phi_y \rrbracket_i) \right) \wedge \llbracket \varphi \rrbracket_k^0 \right)$$

The correctness of the encoding relies on a safe choice of the set Y , falling back to the incomplete lasso-shaped case when some syntactic restrictions on the expressions containing clocks are not met (see Supplemental Material for more details).

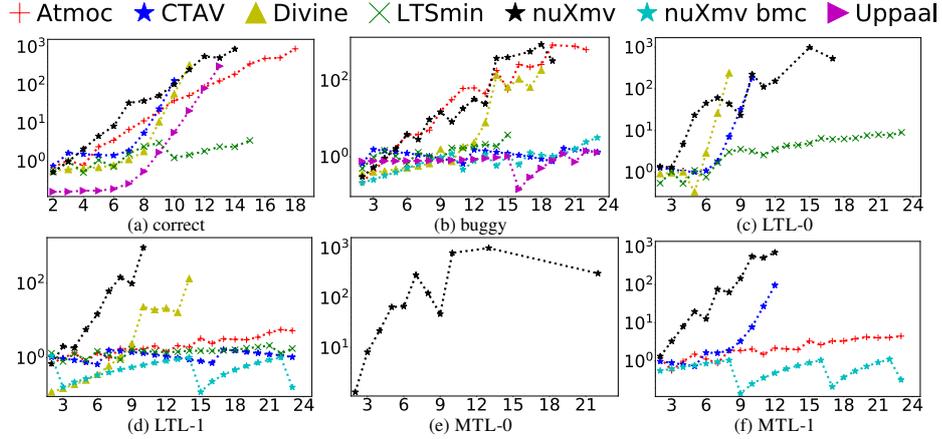


Fig. 3. Runtime for the Fisher mutual exclusion problem; x-axis: number of processes, y-axis: time (s). LTL-1 and MTL-1 properties are the bounded version of resp. LTL-0 and MTL-0.

5 Related Work

There are many tools that allow for the specification and verification of infinite state symbolic synchronous transition systems. Given the focus of this paper, here we restrict our attention to tools supporting timed systems and/or MTL properties.

Uppaal [18], the reference tool for timed systems verification, supports only bounded variable types and therefore finite asynchronous TTS. Properties are limited to a subset of the branching-time logic TCTL [19,20]. LTSmin [21] and Divine [22] are two model checkers that support the Uppaal specification language and properties specified in LTL. RTD-Finder [23] handles only safety properties for real-time component-based systems specified in RT-BIP. The verification is based on a compositional computation of an invariant over-approximating the set of reachable states of the system and leverages on counterexample-based invariant refinement algorithm. The ZOT Bounded Model/Satisfiability Checker [24] supports different logic languages through a multi-layered approach based on LTL with past operators. Similarly to NUXMV, ZOT supports dense-time MTL. It leverages only on SMT-based Bounded Model Checking, and is therefore unable to prove that properties hold. ATMOC [25] implements an extension of IC3 [10] and K-induction [26] to deal with symbolic timed transition systems. It supports both invariant and $MTL_{0,\infty}$ properties, although for the latter it only supports bounded model checking. CTAV [27] reduces the model checking problem for an $MTL_{0,\infty}$ property φ to a symbolic language emptiness check of a timed Büchi automata for φ .

Differently from all the above tools NUXMV is able to prove $MTL_{0,\infty}$ properties on timed transition systems with infinite domain variables

6 Experimental Evaluation

We compared NUXMV with ATMOC [25], CTAV [27], ZOT [24], Divine [22], LTSmin [21], and Uppaal [28].

For the evaluation we considered i) scalable benchmarks taken from competitor tools distributions and from the literature; ii) handcrafted benchmarks to stress various language features. In particular, we considered different versions of the Fisher mutual exclusion protocol (correct and buggy) with different properties, different versions of the emergency diesel generator problem (previously studied with ATMOC [25]). Finally we considered also the validity checks of some MTL properties also taken from [25]. We run all the experiments on a PC equipped with a 3.7GHz Xeon quad core CPU and 16Gb of RAM, using a time/memory limit of 1000sec/10Gb for each test.

The results of the evaluation are reported in Fig. 3 for the Fisher family of experiments, and in Fig. 4 for the emergency diesel generator family of problems (CTAV does not appear in the plot of MTL-0 because it wrongly reports a counterexample although MTL-0 is the bounded version of LTL-0). While the results for the validity check of pure MTL properties are reported in Fig. 5 In the plots NUXMV refers to runtime for the IC3 with implicit abstraction in lockstep with BMC with the modified loop condition algorithm, and NUXMV-bmc refers to runtime for BMC alone with the modified loop condition algorithm. The results show that NUXMV is competitive with and in many cases performs better than other state-of-the-art tools, especially on validity problems for $MTL_{0,\infty}$.

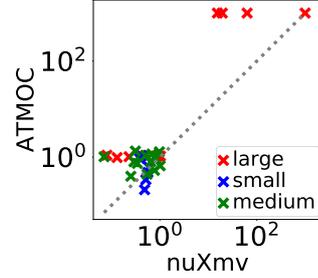


Fig. 4. Result for the runtime (s) for the diesel generator problem.

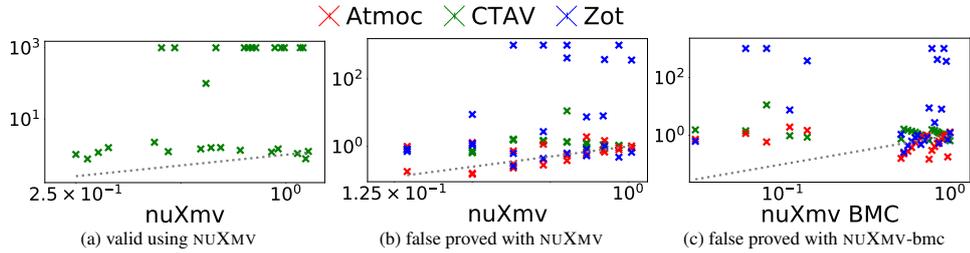


Fig. 5. Runtime (s) for the validity checks of MTL properties.

7 Conclusions

We presented the new version of NUXMV, a state-of-the art symbolic model checker for finite and infinite-state transition systems, that we extended to allow for the specification of synchronous timed transition systems and of $MTL_{0,\infty}$ properties. To support the new features, we extended the NUXMV language, we allowed for the specification $MTL_{0,\infty}$ formulas with parametric intervals, we adapted the model checking algorithms to find for lasso-shaped traces (over discrete semantics) where clocks may diverge. We evaluated the new features comparing NUXMV with other verification tools for timed automata, considering different benchmarks. The results show that NUXMV is competitive with and in many cases performs better than state-of-the-art tools, especially on validity problems for $MTL_{0,\infty}$.

References

1. Cavada, R., Cimatti, A., Dorigatti, M., Griggio, A., Mariotti, A., Micheli, A., Mover, S., Roveri, M., Tonetta, S.: The nuXmv Symbolic Model Checker. In Biere, A., Bloem, R., eds.: *Computer Aided Verification - 26th International Conference, CAV 2014, Held as Part of the Vienna Summer of Logic, VSL 2014, Vienna, Austria, July 18-22, 2014. Proceedings.* Volume 8559 of *Lecture Notes in Computer Science.*, Springer (2014) 334–342
2. Cimatti, A., Griggio, A., Schaafsma, B.J., Sebastiani, R.: The MathSAT5 SMT Solver. In Piterman, N., Smolka, S.A., eds.: *TACAS. Volume 7795 of LNCS.*, Springer (2013) 93–107
3. Cimatti, A., Griggio, A., Irfan, A., Roveri, M., Sebastiani, R.: Incremental linearization for satisfiability and verification modulo nonlinear arithmetic and transcendental functions. *ACM Trans. Comput. Log.* **19**(3) (2018) 19:1–19:52
4. Cimatti, A., Griggio, A., Mover, S., Tonetta, S.: Infinite-state invariant checking with IC3 and predicate abstraction. *Formal Methods in System Design* **49**(3) (2016) 190–218
5. Daniel, J., Cimatti, A., Griggio, A., Tonetta, S., Mover, S.: Infinite-state liveness-to-safety via implicit abstraction and well-founded relations. In Chaudhuri, S., Farzan, A., eds.: *Computer Aided Verification - 28th International Conference, CAV 2016, Toronto, ON, Canada, July 17-23, 2016, Proceedings, Part I. Volume 9779 of Lecture Notes in Computer Science.*, Springer (2016) 271–291
6. Koymans, R.: Specifying real-time properties with metric temporal logic. *Real-Time Systems* **2**(4) (Nov 1990) 255–299
7. Ouaknine, J., Worrell, J.: On the decidability of metric temporal logic. In: *Logic in Computer Science, 2005. LICS 2005. Proceedings. 20th Annual IEEE Symposium on, IEEE (2005)* 188–197
8. Somenzi, F.: CUDD: Colorado University Decision Diagram package — release 2.4.1
9. Eén, N., Sörensson, N.: An extensible sat-solver. In Giunchiglia, E., Tacchella, A., eds.: *SAT. Volume 2919 of LNCS.*, Springer (2003) 502–518
10. Bradley, A.R.: Sat-based model checking without unrolling. In Jhala, R., Schmidt, D.A., eds.: *VMCAI. Volume 6538 of LNCS.*, Springer (2011) 70–87
11. Hassan, Z., Bradley, A.R., Somenzi, F.: Better generalization in ic3. In: *FMCAD, IEEE (2013)* 157–164
12. Vizek, Y., Grumberg, O., Shoham, S.: Lazy abstraction and sat-based reachability in hardware model checking. In Cabodi, G., Singh, S., eds.: *FMCAD, IEEE (2012)* 173–181
13. Claessen, K., Sörensson, N.: A liveness checking algorithm that counts. In Cabodi, G., Singh, S., eds.: *FMCAD, IEEE (2012)* 52–59
14. Schuppan, V., Biere, A.: Liveness checking as safety checking for infinite state spaces. *Electr. Notes Theor. Comput. Sci.* **149**(1) (2006) 79–96
15. Alur, R., Feder, T., Henzinger, T.A.: The Benefits of Relaxing Punctuality. *J. ACM* **43**(1) (1996) 116–146
16. Tonetta, S.: Linear-time Temporal Logic with Event Freezing Functions. In: *GandALF. (2017)* 195–209
17. Biere, A., Cimatti, A., Clarke, E.M., Strichman, O., Zhu, Y.: Bounded model checking. *Advances in Computers* **58** (2003) 117–148
18. Behrmann, G., David, A., Larsen, K.G.: A tutorial on UPPAAL. In Bernardo, M., Corradini, F., eds.: *Formal Methods for the Design of Real-Time Systems: 4th International School on Formal Methods for the Design of Computer, Communication, and Software Systems, SFM-RT 2004. Number 3185 in LNCS, Springer-Verlag (September 2004)* 200–236
19. Bouyer, P.: Model-checking timed temporal logics. In Areces, C., Demri, S., eds.: *Proceedings of the 4th Workshop on Methods for Modalities (M4M-5). Volume 231 of Electronic Notes in Theoretical Computer Science.*, Cachan, France, Elsevier Science Publishers (March 2009) 323–341
20. Bouyer, P., Laroussinie, F., Markey, N., Ouaknine, J., Worrell, J.: Timed temporal logics. In Aceto, L., Bacci, G., Bacci, G., Ingólfssdóttir, A., Legay, A., Mardare, R., eds.: *Models, Algorithms, Logics and Tools: Essays Dedicated to Kim Guldstrand Larsen on the Occasion of*

- His 60th Birthday. Volume 10460 of Lecture Notes in Computer Science. Springer (August 2017) 211–230
21. Kant, G., Laarman, A., Meijer, J., van de Pol, J., Blom, S., van Dijk, T.: LTSmin: High-Performance Language-Independent Model Checking. In Baier, C., Tinelli, C., eds.: Tools and Algorithms for the Construction and Analysis of Systems - 21st International Conference, TACAS 2015, Held as Part of the European Joint Conferences on Theory and Practice of Software, ETAPS 2015, London, UK, April 11-18, 2015. Proceedings. Volume 9035 of Lecture Notes in Computer Science., Springer (2015) 692–707
 22. Baranová, Z., Barnat, J., Kejstová, K., Kučera, T., Lauko, H., Mrázek, J., Ročkait, P., Štill, V.: Model checking of C and C++ with DIVINE 4. In: Automated Technology for Verification and Analysis (ATVA 2017). Volume 10482 of LNCS., Springer (2017) 201–207
 23. Rayana, S.B., Bozga, M., Bensalem, S., Combaz, J.: Rtd-finder: A tool for compositional verification of real-time component-based systems. In Chechik, M., Raskin, J., eds.: Tools and Algorithms for the Construction and Analysis of Systems - 22nd International Conference, TACAS 2016, Held as Part of the European Joint Conferences on Theory and Practice of Software, ETAPS 2016, Eindhoven, The Netherlands, April 2-8, 2016, Proceedings. Volume 9636 of Lecture Notes in Computer Science., Springer (2016) 394–406
 24. Pradella, M.: A user’s guide to zot. CoRR **abs/0912.5014** (2009)
 25. Kindermann, R., Junttila, T.A., Niemelä, I.: Smt-based induction methods for timed systems. CoRR **abs/1204.5639** (2012)
 26. Sheeran, M., Singh, S., Stålmarck, G.: Checking safety properties using induction and a sat-solver. In Jr., W.A.H., Johnson, S.D., eds.: FMCAD. Volume 1954 of LNCS., Springer (2000) 108–125
 27. Li, G.: Checking timed büchi automata emptiness using lu-abstractions. In Ouaknine, J., Vaandrager, F.W., eds.: Formal Modeling and Analysis of Timed Systems, 7th International Conference, FORMATS 2009, Budapest, Hungary, September 14-16, 2009. Proceedings. Volume 5813 of Lecture Notes in Computer Science., Springer (2009) 228–242
 28. Larsen, K.G., Lorber, F., Nielsen, B.: 20 years of UPPAAL enabled industrial model-based validation and beyond. In Margaria, T., Steffen, B., eds.: Leveraging Applications of Formal Methods, Verification and Validation. Industrial Practice - 8th International Symposium, ISoLA 2018, Limassol, Cyprus, November 5-9, 2018, Proceedings, Part IV. Volume 11247 of Lecture Notes in Computer Science., Springer (2018) 212–229